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Board Design ...

Typical signal integrity issues at the package level, such as signal reflection, delay, crosstalk, ground bounce, and voltage drop are reduced by careful consideration of the geometries and ...

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interdependence among digital and analog signal integrity issues For example, a slow rise time on a gate input can cause the output pulse to be delayed, in turn causing a bus contention in the digital environment further downstream A thorough solution for signal integrity measurement and

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## Design

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Again, each harmonic of the digital signal will be attenuated according to that frequency of operation. Increased loss of the signal components will add to the distortion of the digital signal. Printed circuit board layout is often the single most important factor affecting the electromagnetic compatibility of electronic systems. Boards that are auto-routed or laid out according to a list of "design rules" do not usually meet electromagnetic compatibility requirements on the first pass; and ...

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analog in nature, and all signals are subject to effects such as noise, distortion, and loss. Over short distances and at low bit rates, a simple conductor can transmit this with sufficient fidelity. At high bit rates and over longer distances or through various mediums, various effec

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